Refine Search

Search Results -

Terms	Documents
((high adj1 speed) near5 bus) near10 (chipset or chip or IC)	204

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L1 and (serial$2 near5 (storage or
memory))
```

[Refine Search]

[Recall Text ⬍]　　[Clear]　　[Interrupt]

Search History

DATE: Thursday, June 24, 2004　　Printable Copy　　Create Case

Set Name Query	Hit Count	Set Name
side by side		result set

DB=USPT, USOC; PLUR=YES; OP=OR

| L1 | ((high adj1 speed) near5 bus) near10 (chipset or chip or IC) | 204 | L1 |

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2 and (control$4 near15 port)	7

Database:

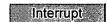US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text   Clear   Interrupt

Search History

DATE: Thursday, June 24, 2004   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
	side by side		result set
	DB=USPT,USOC; PLUR=YES; OP=OR		
L3	L2 and (control$4 near15 port)	7	L3
L2	L1 and (serial$2 near5 (storage or memory))	15	L2
L1	((high adj1 speed) near5 bus) near10 (chipset or chip or IC)	204	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch

Refine Search

Search Results -

Terms	Documents
L3	0

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L4
```

Refine Search

Recall Text ◆   Clear   Interrupt

Search History

DATE: Thursday, June 24, 2004   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L4	L3	0	L4
	DB=USPT,USOC; PLUR=YES; OP=OR		
L3	L2 and (control$4 near15 port)	7	L3
L2	L1 and (serial$2 near5 (storage or memory))	15	L2
L1	((high adj1 speed) near5 bus) near10 (chipset or chip or IC)	204	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch